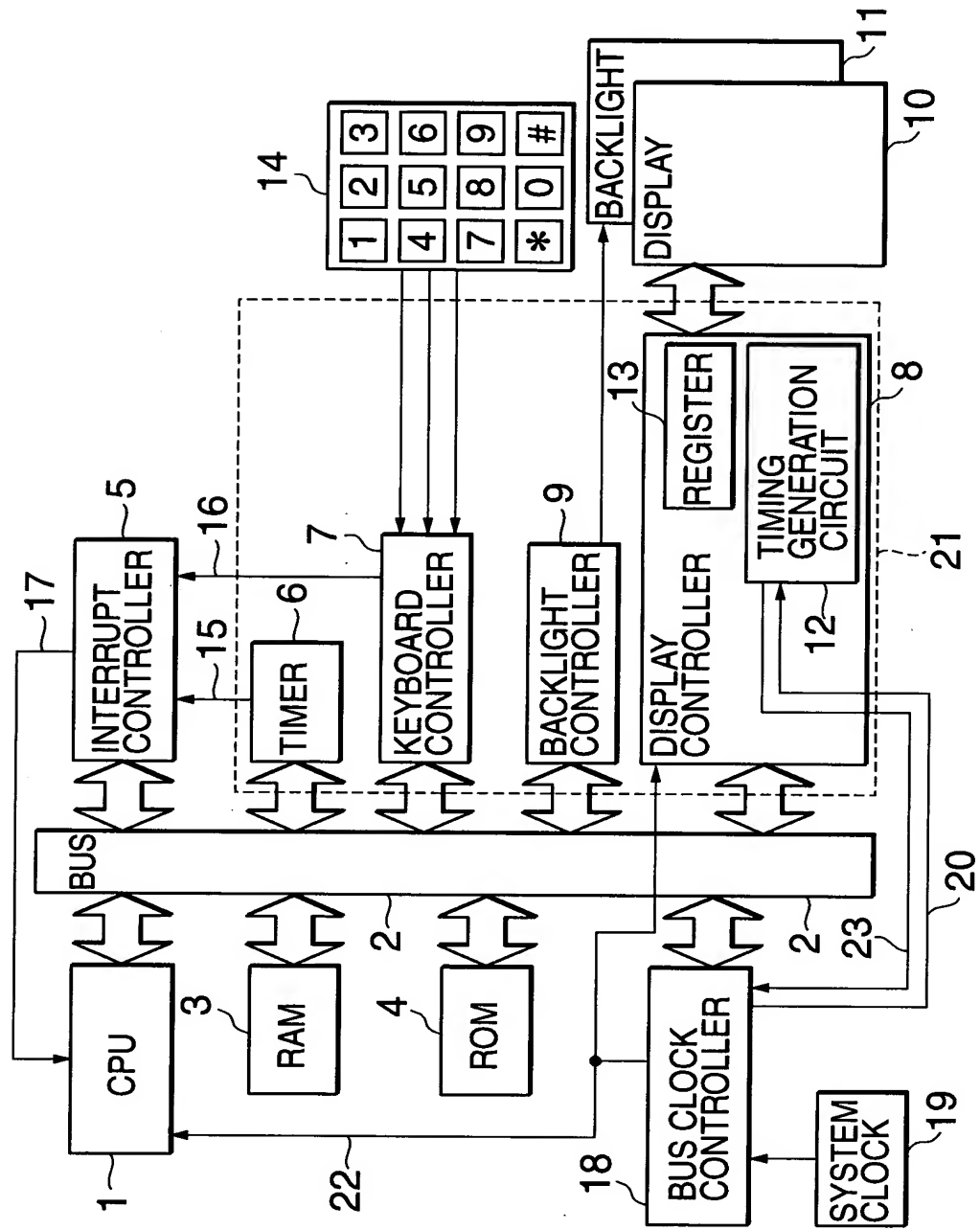


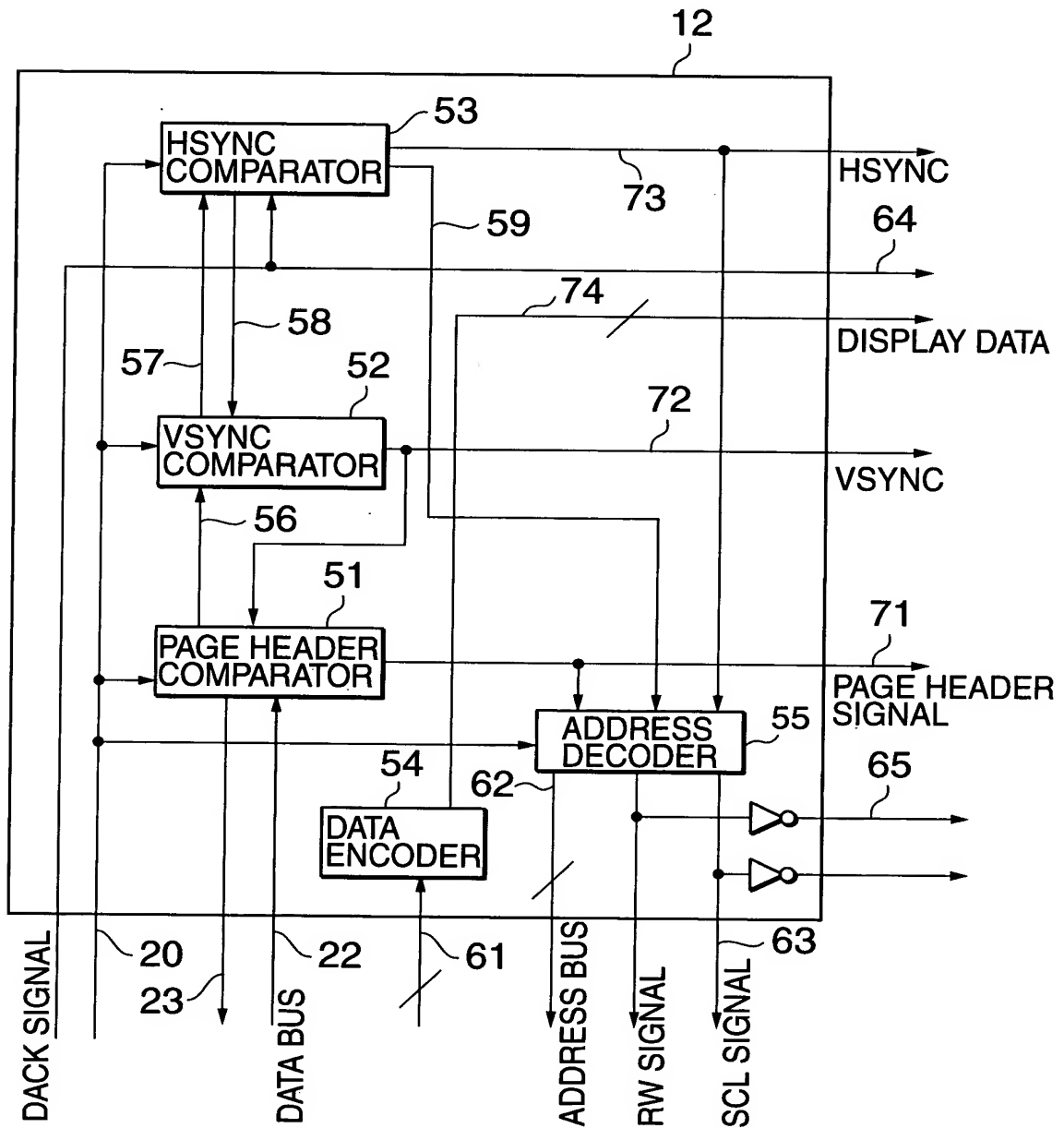
1/6

FIG. 1



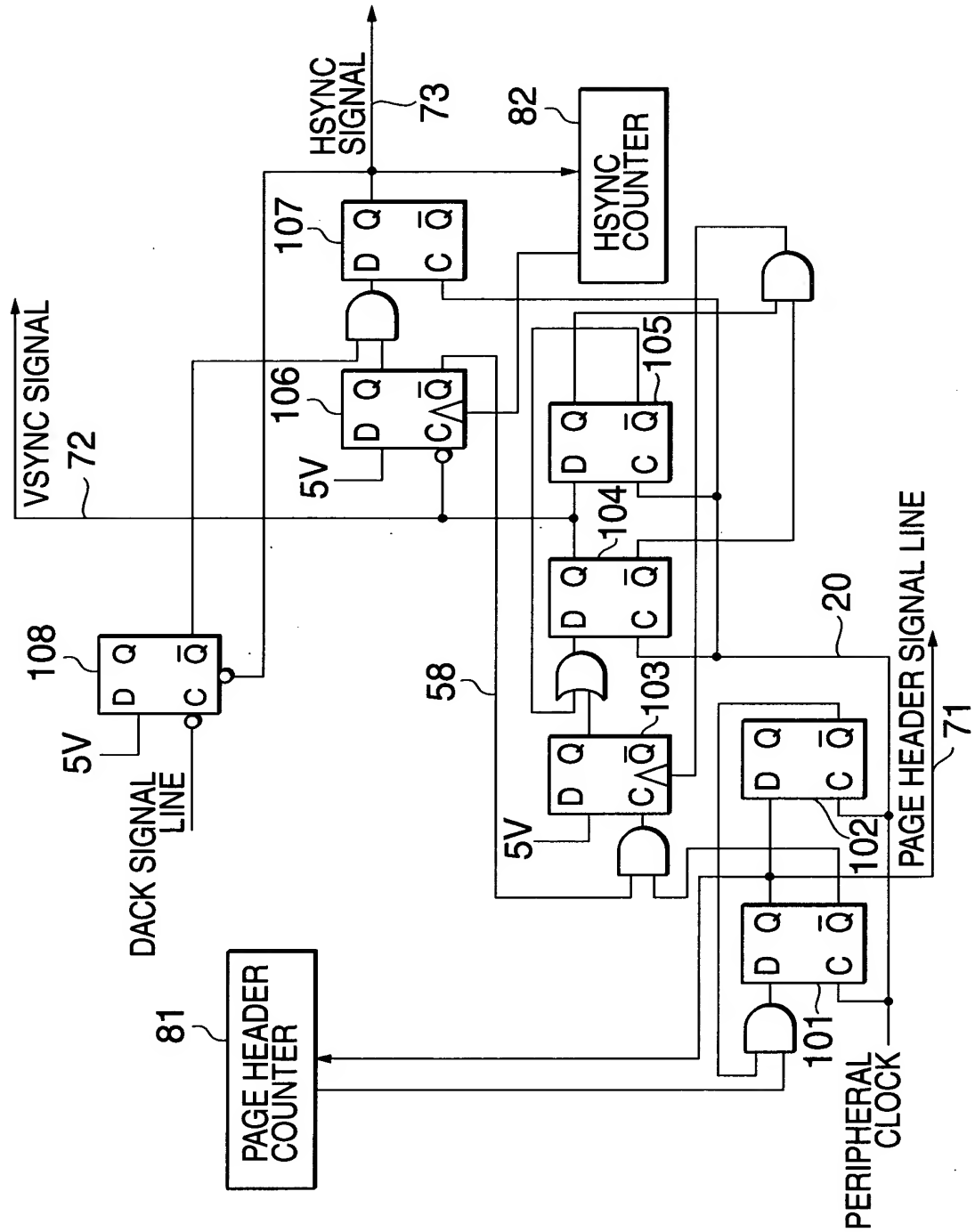
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FIG. 2



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FIG. 3



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FIG. 4

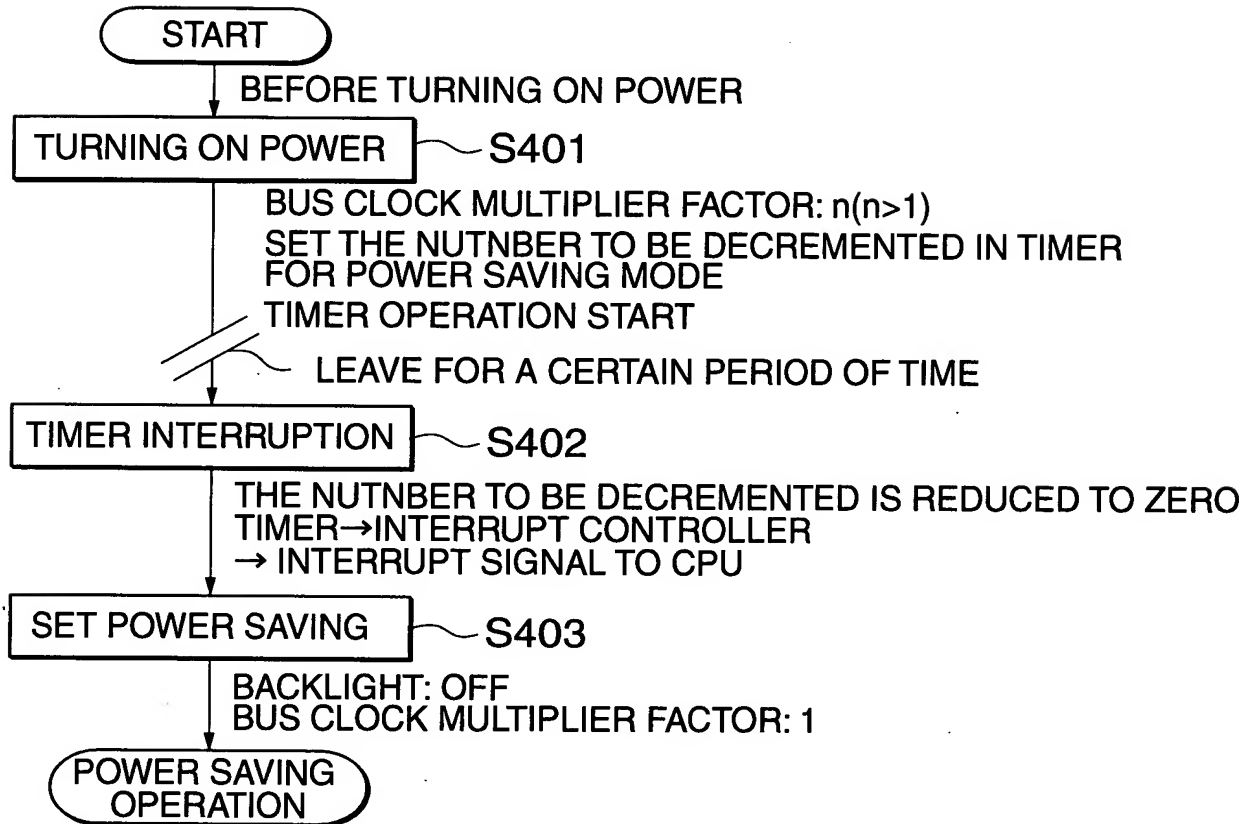
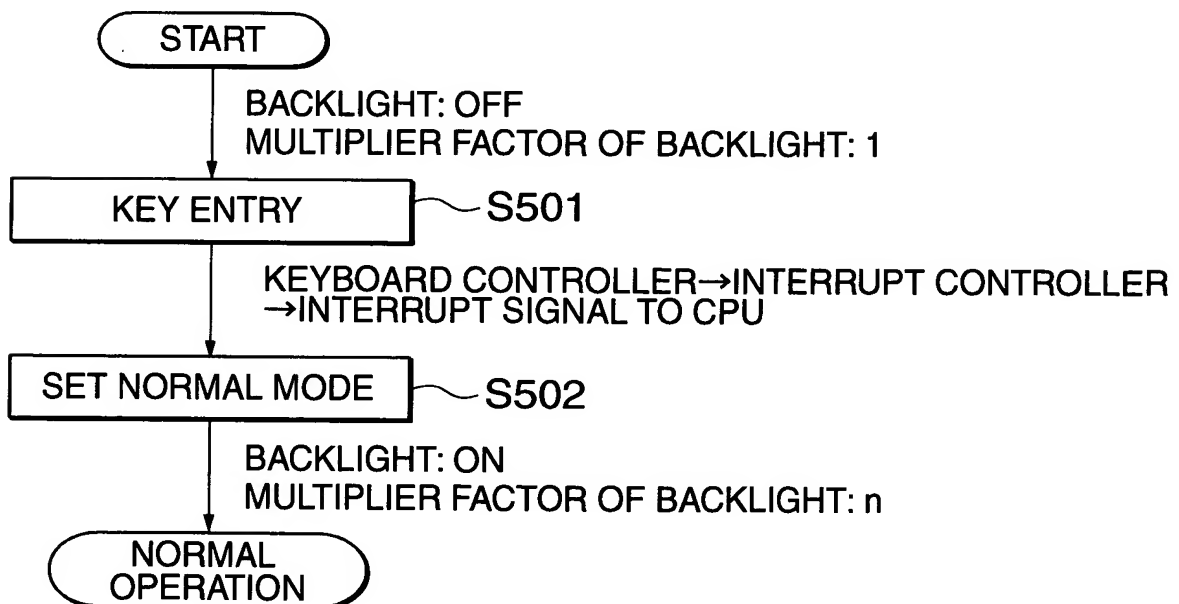
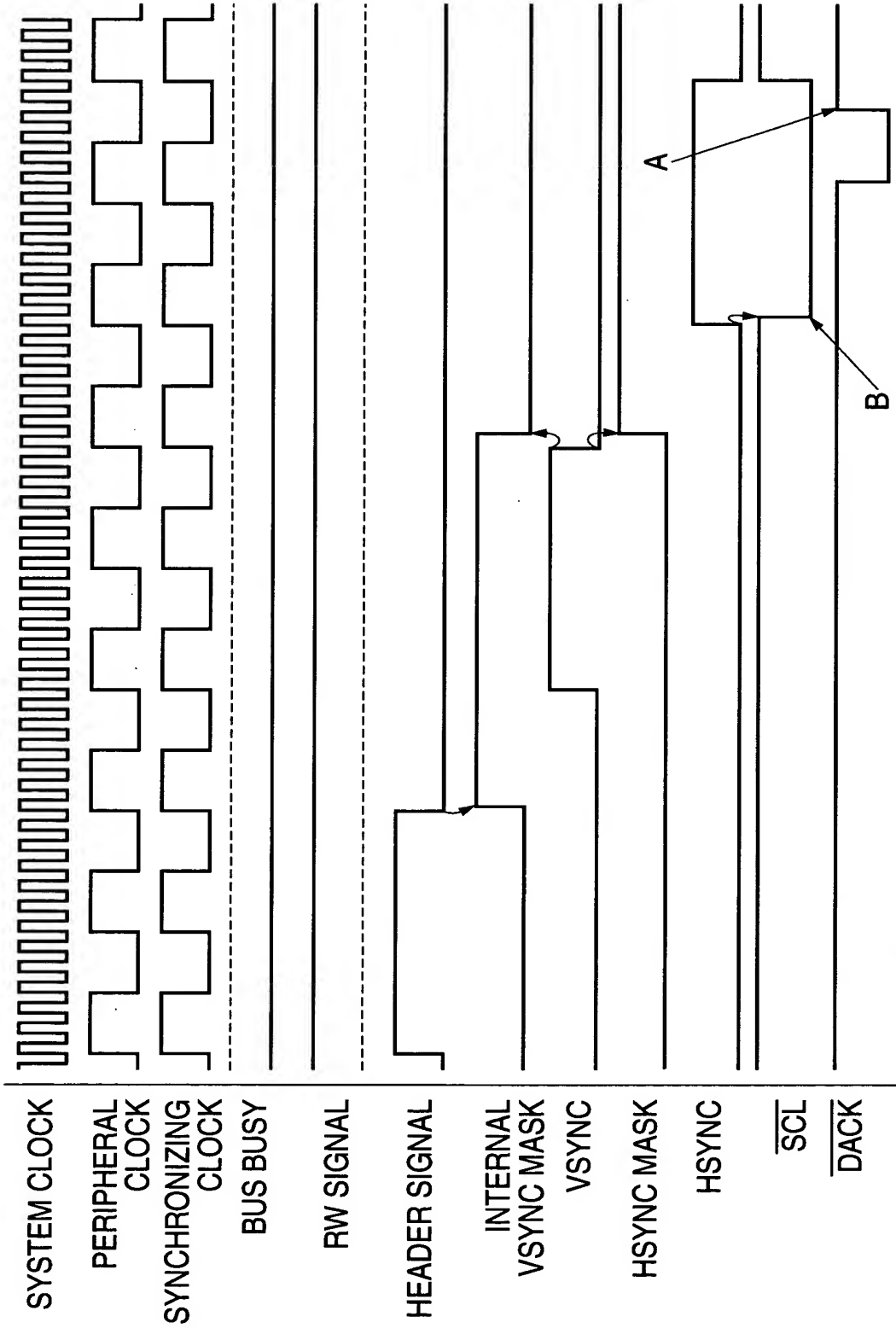


FIG. 5



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FIG. 6



CONTROL SIGNAL LINES OF DISPLAY CONTROLLER IN REFRESHING DISPLAY  
(SYNCHRONIZATION CLOCK WITHDRAWN FROM CONSIDERATION)

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FIG. 7

